

FIG. 3

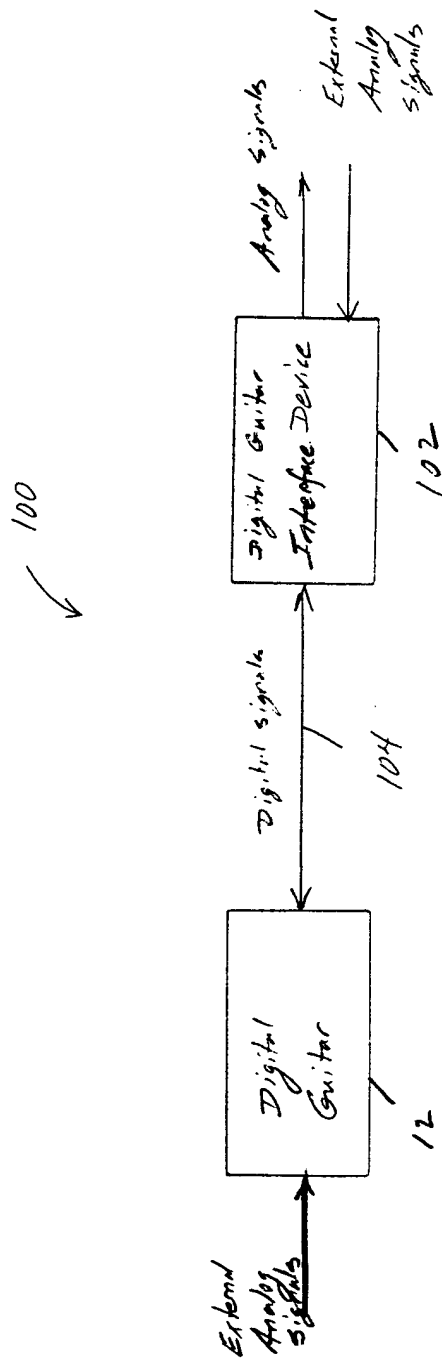


Fig. 4

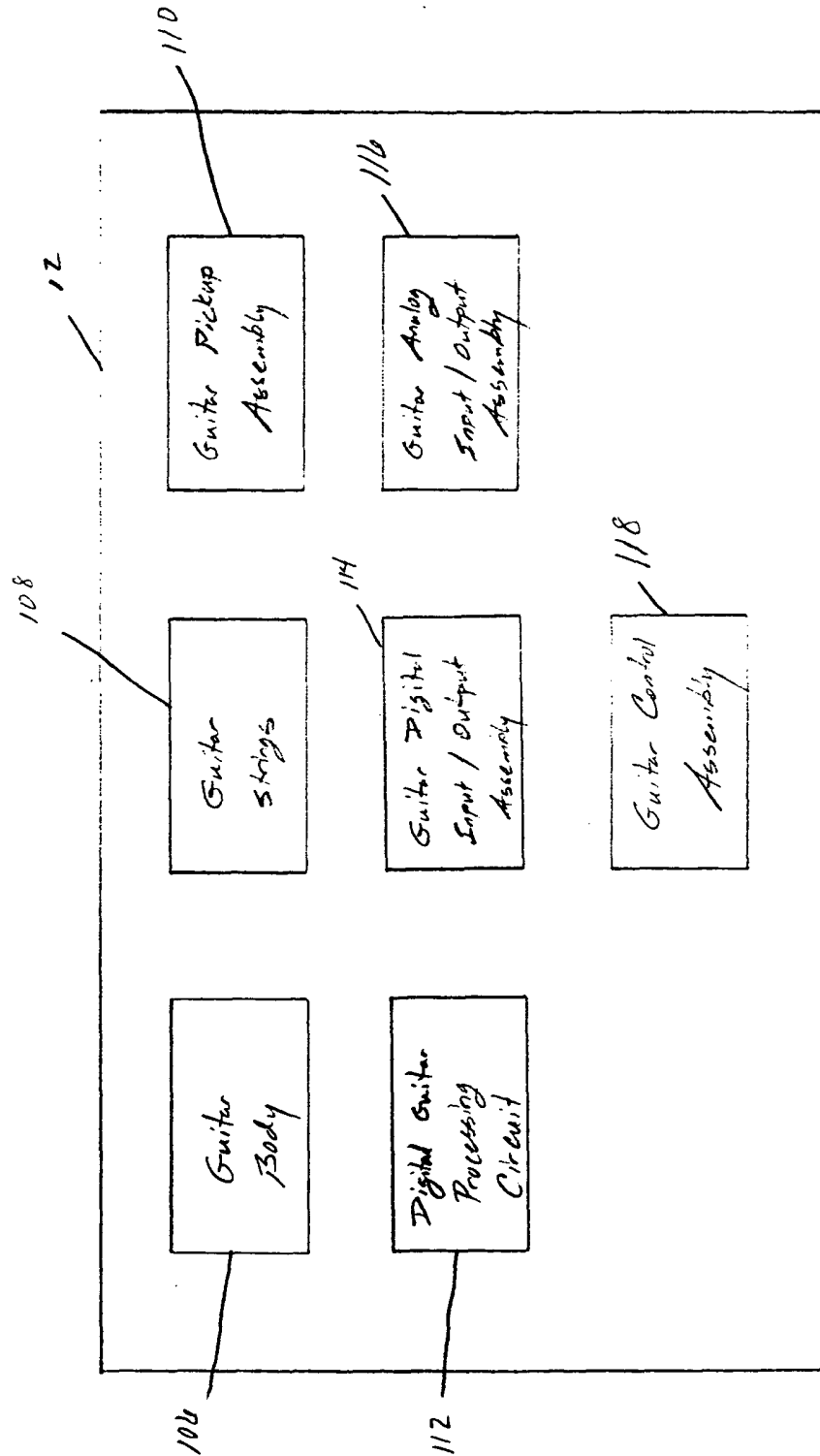


Fig. 5

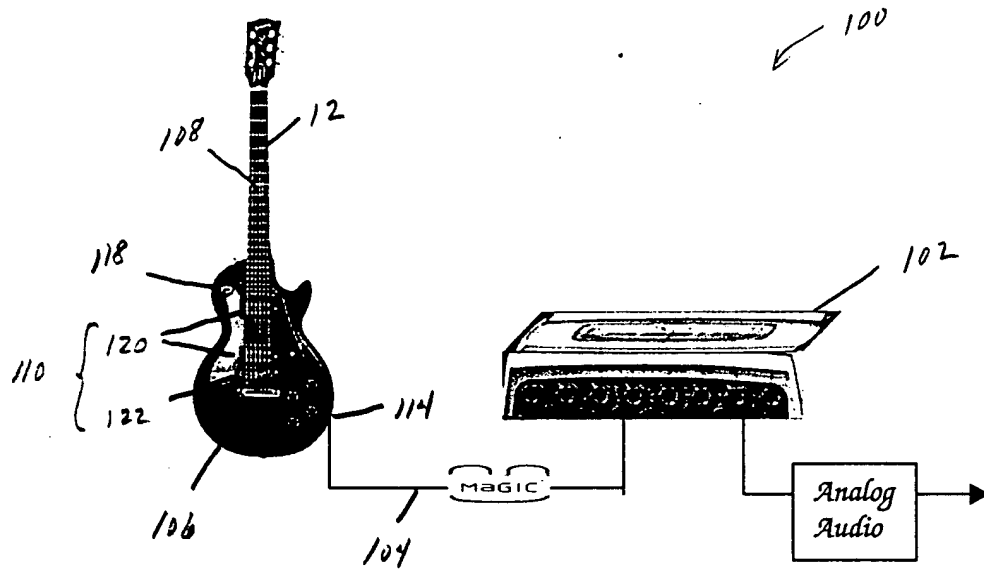


Figure 6

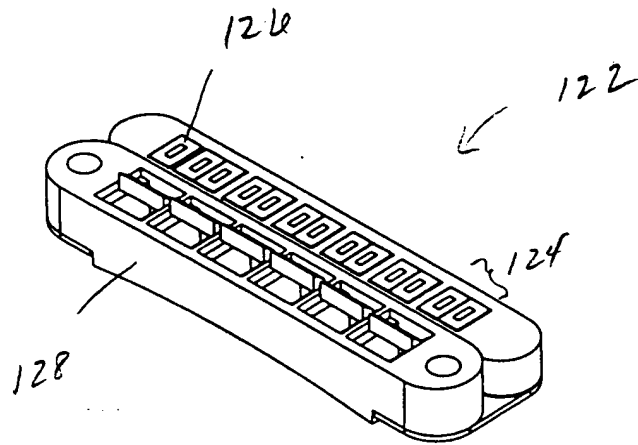


Fig. 7

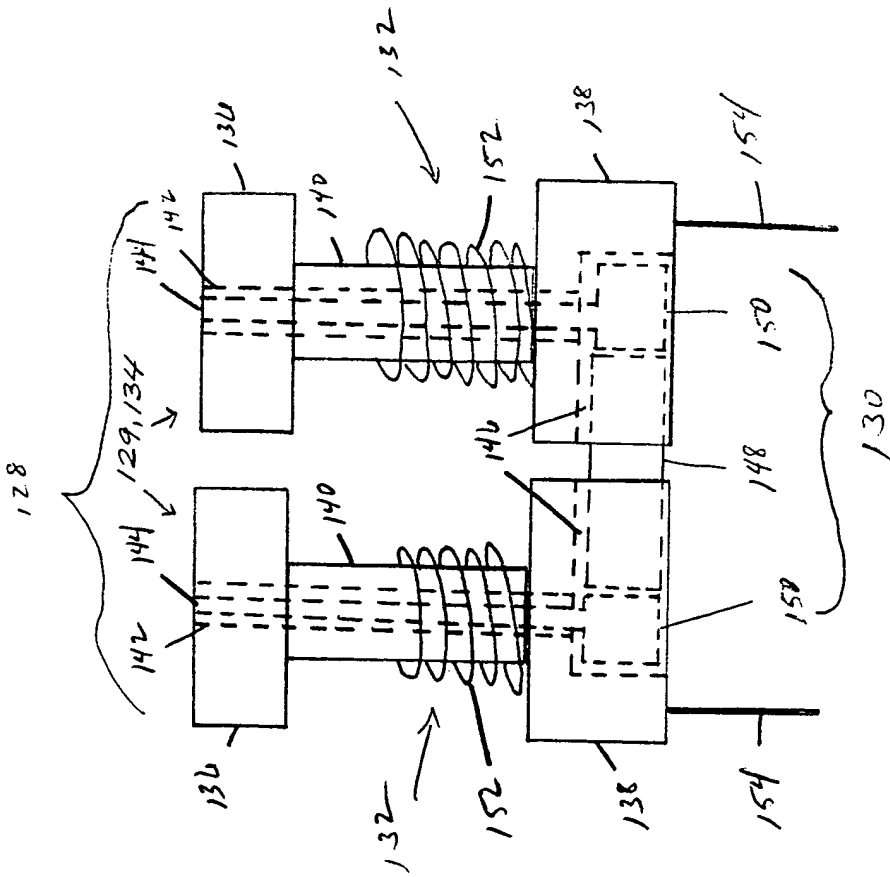


Fig. 8

Digital Guitar Processing Circuit 112

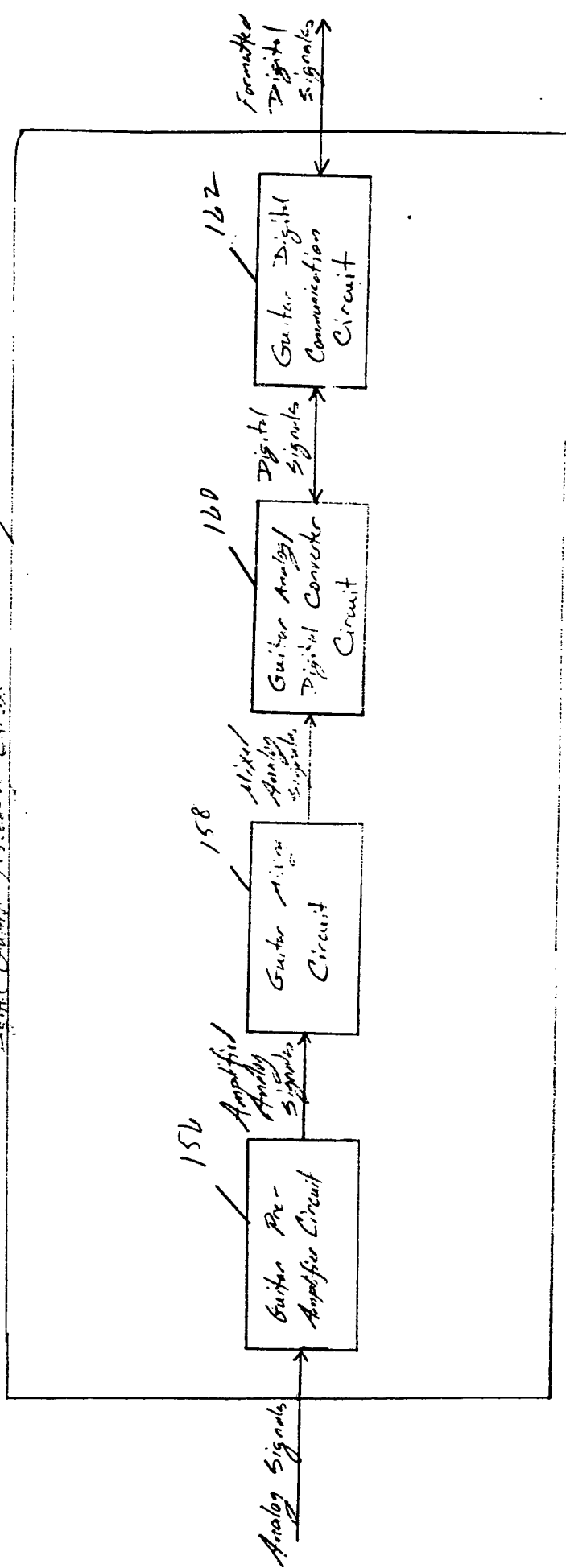


Fig. 9



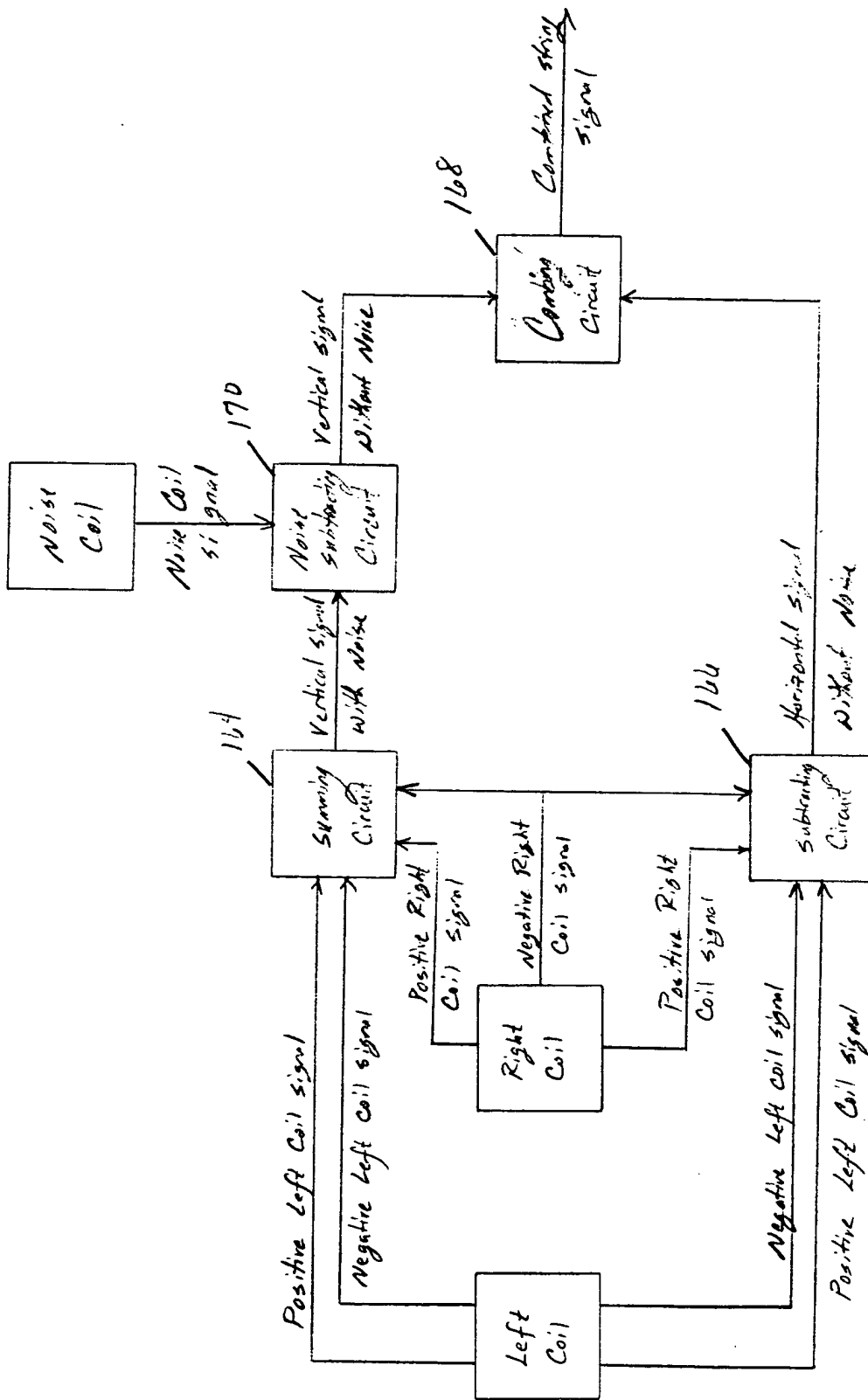


Fig. 10

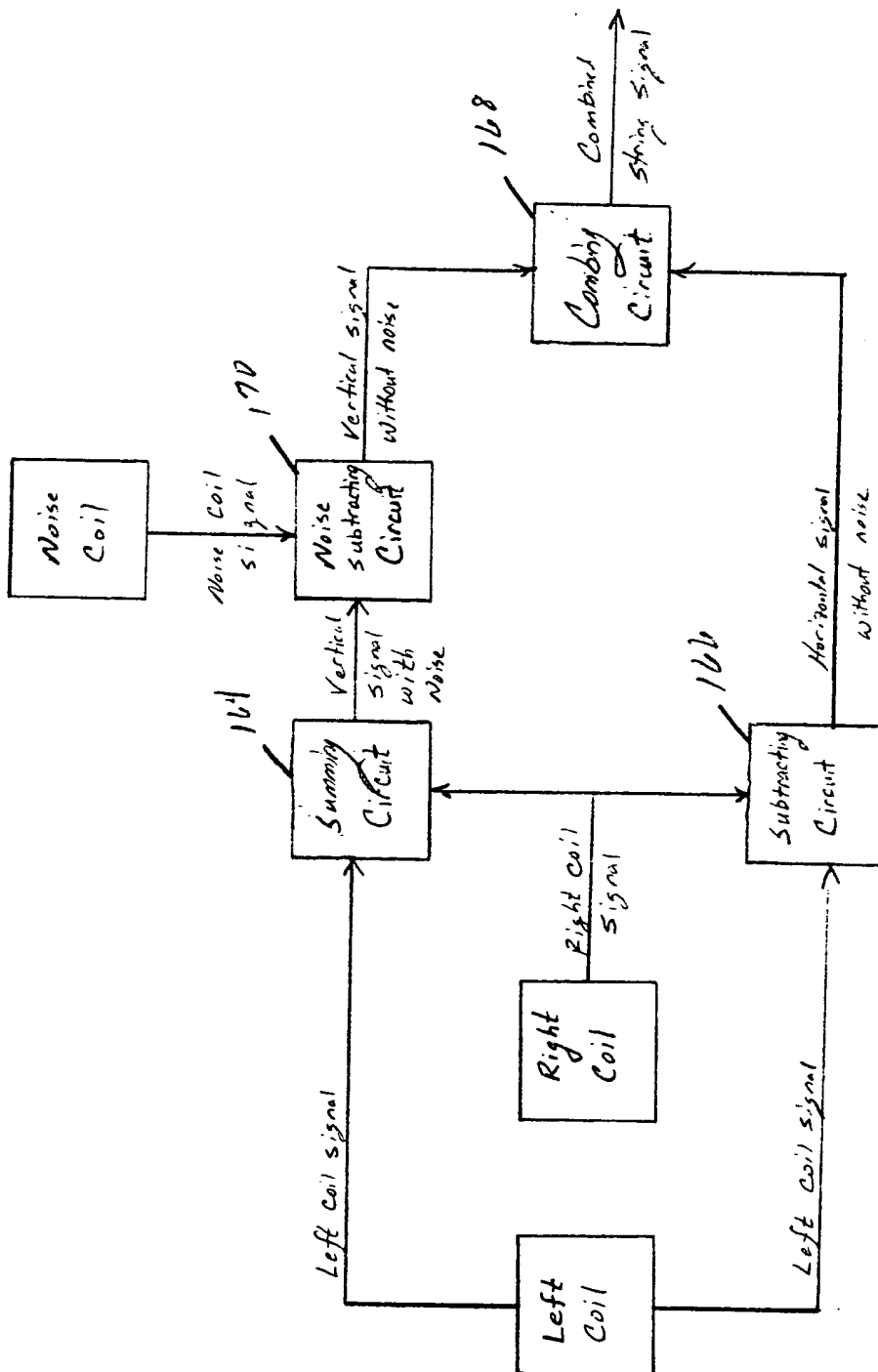


Fig. 11

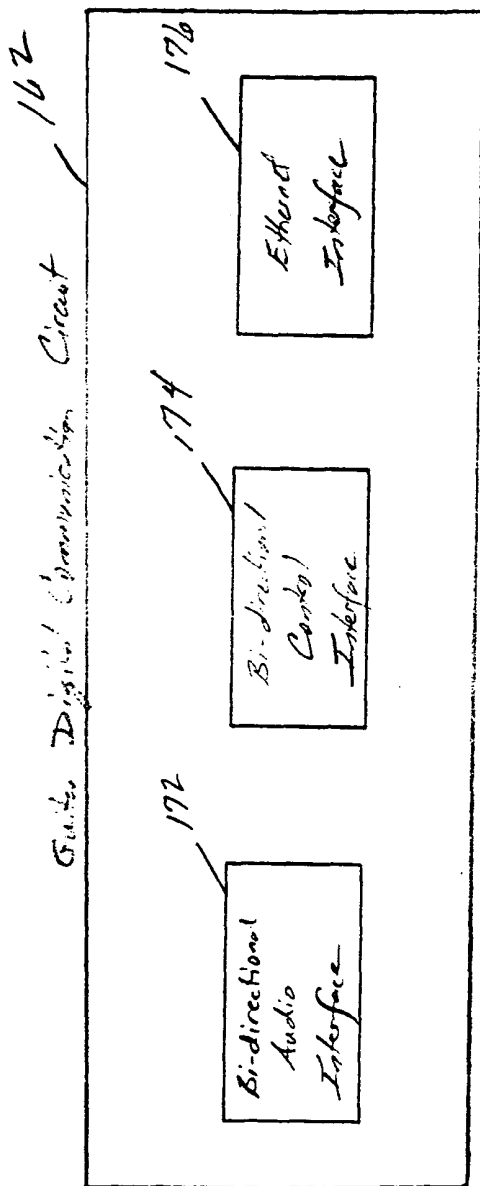


Fig 12

Figure 13

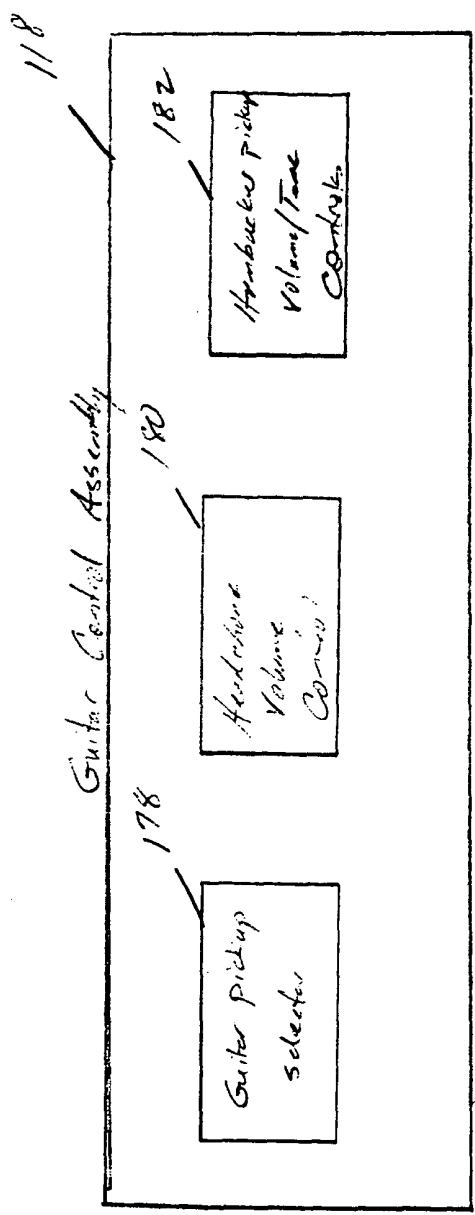


Fig. 14

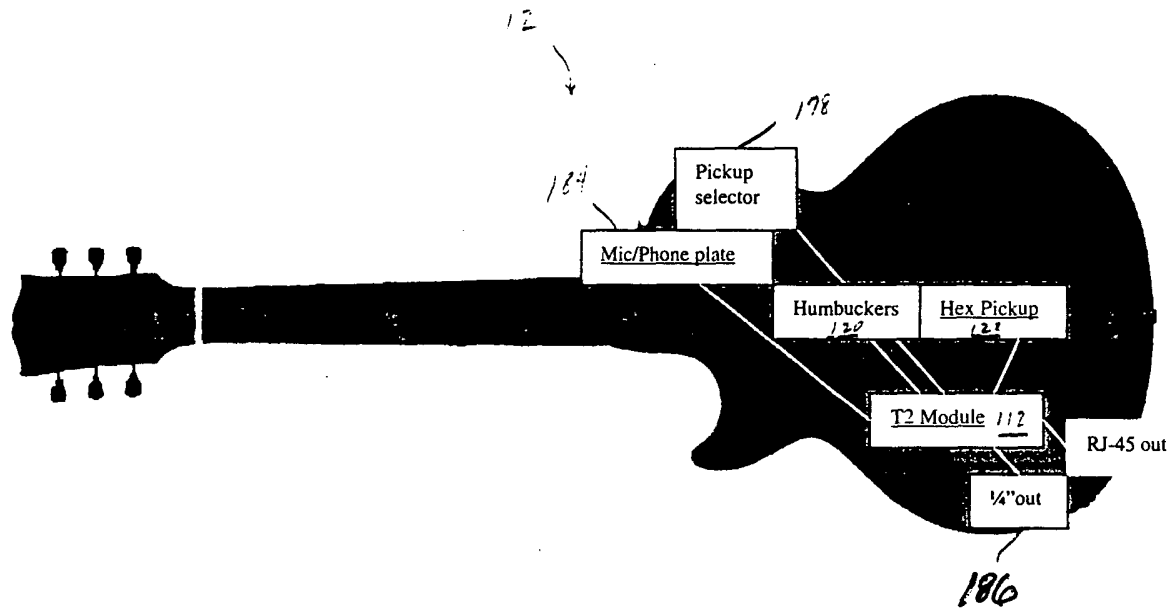


Figure 15

## Preamp Section

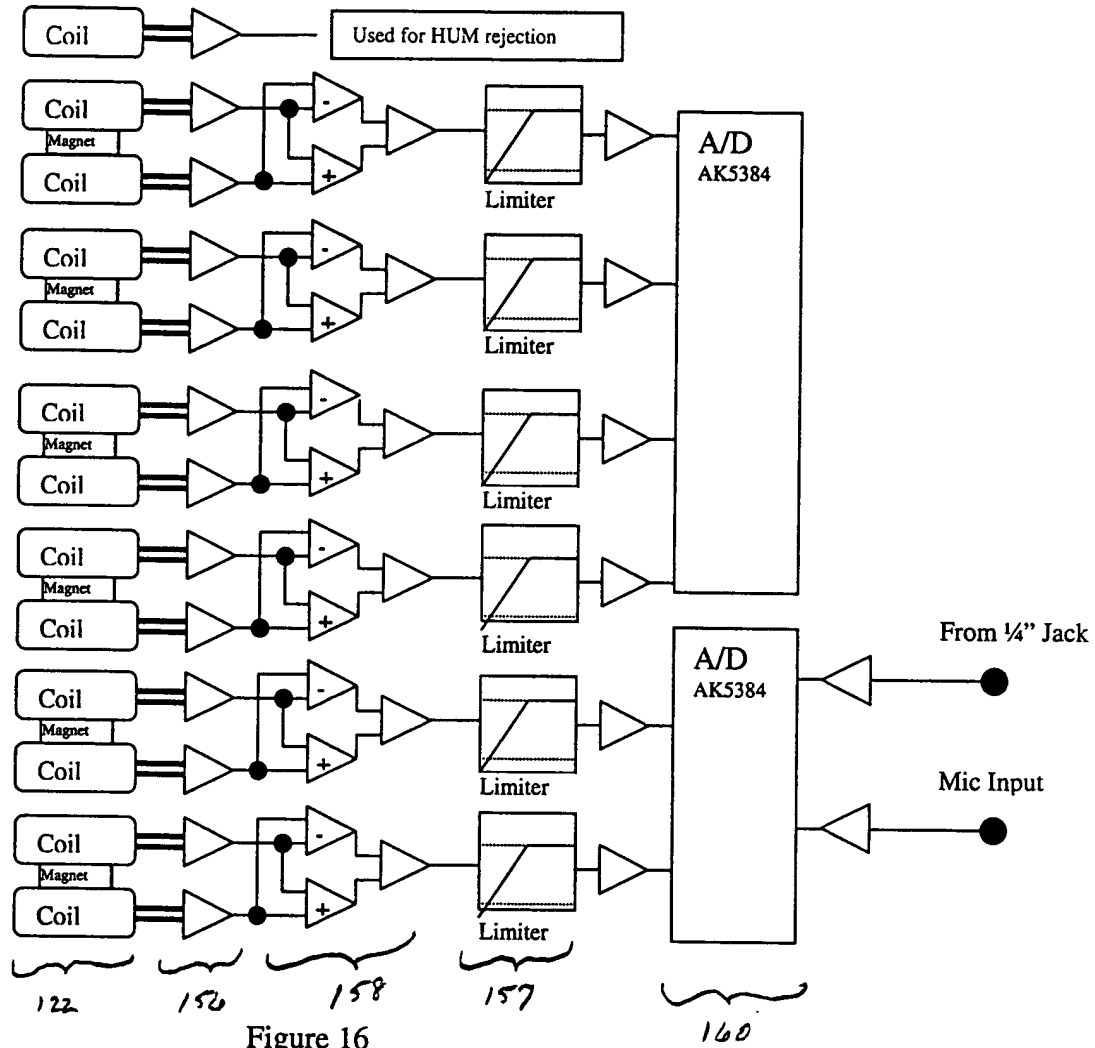


Figure 16

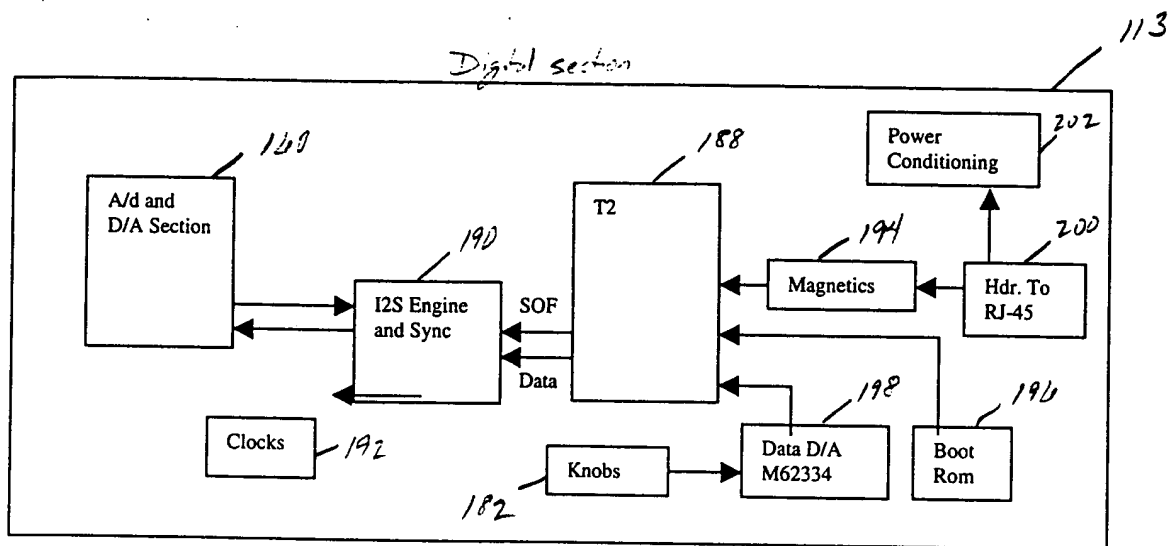


Figure 17



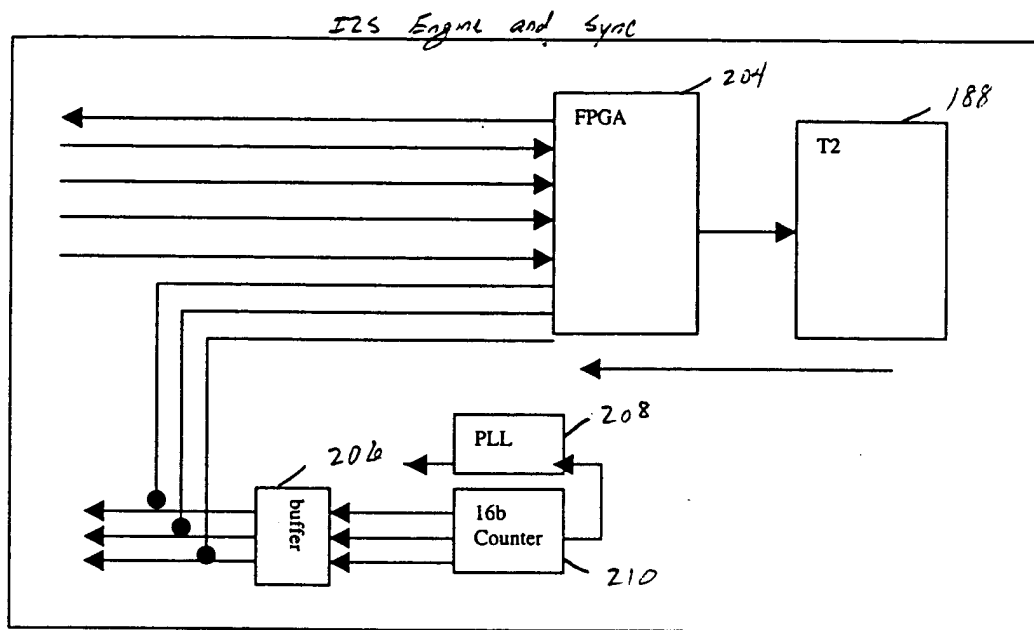


Figure 18

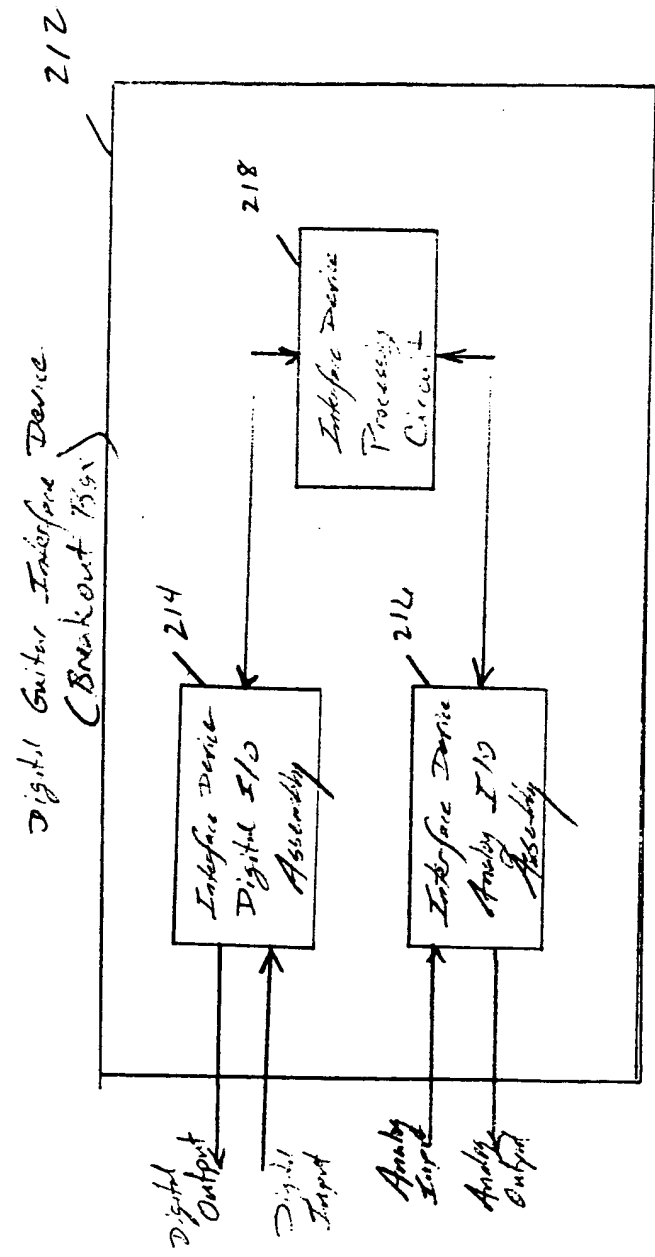


Fig. 19

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Interface Device Processing Circuit

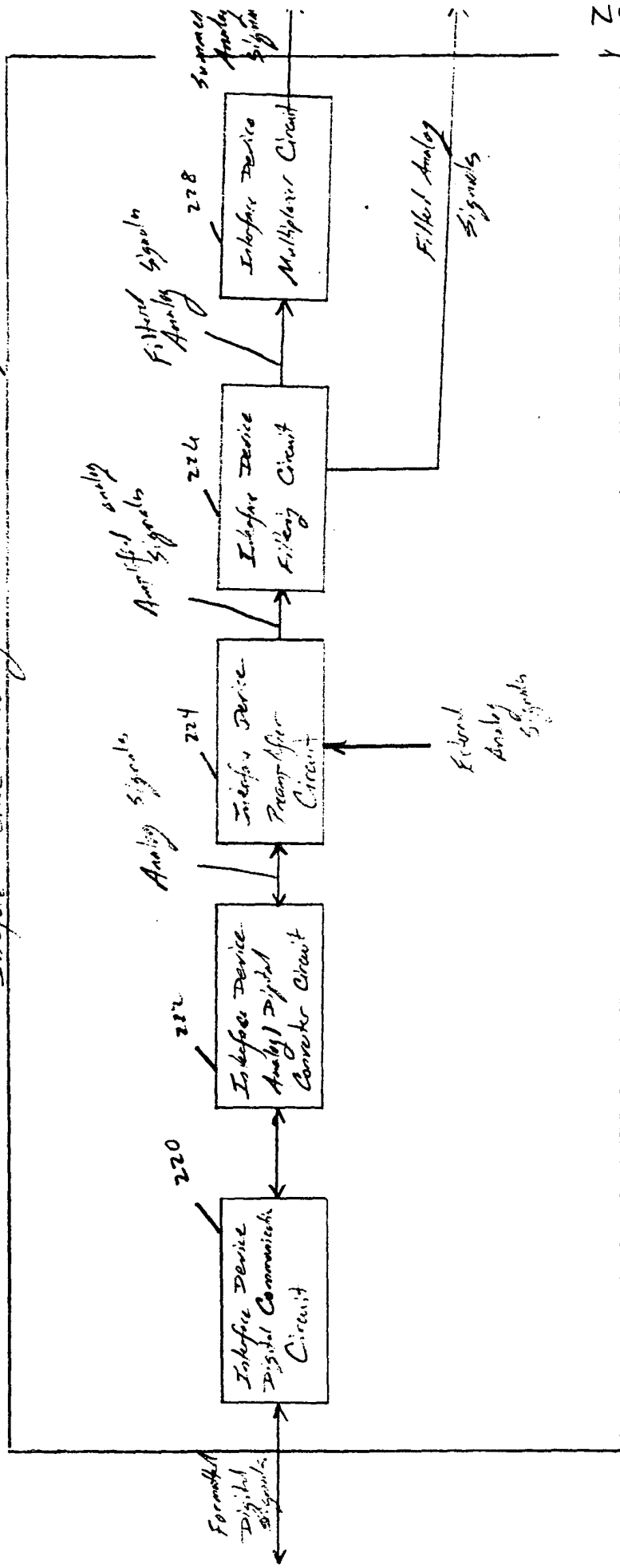
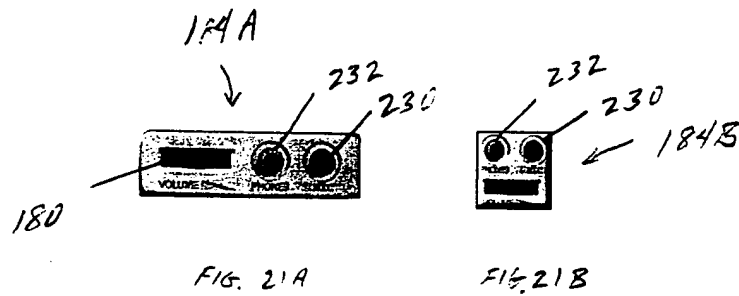


Fig. 20



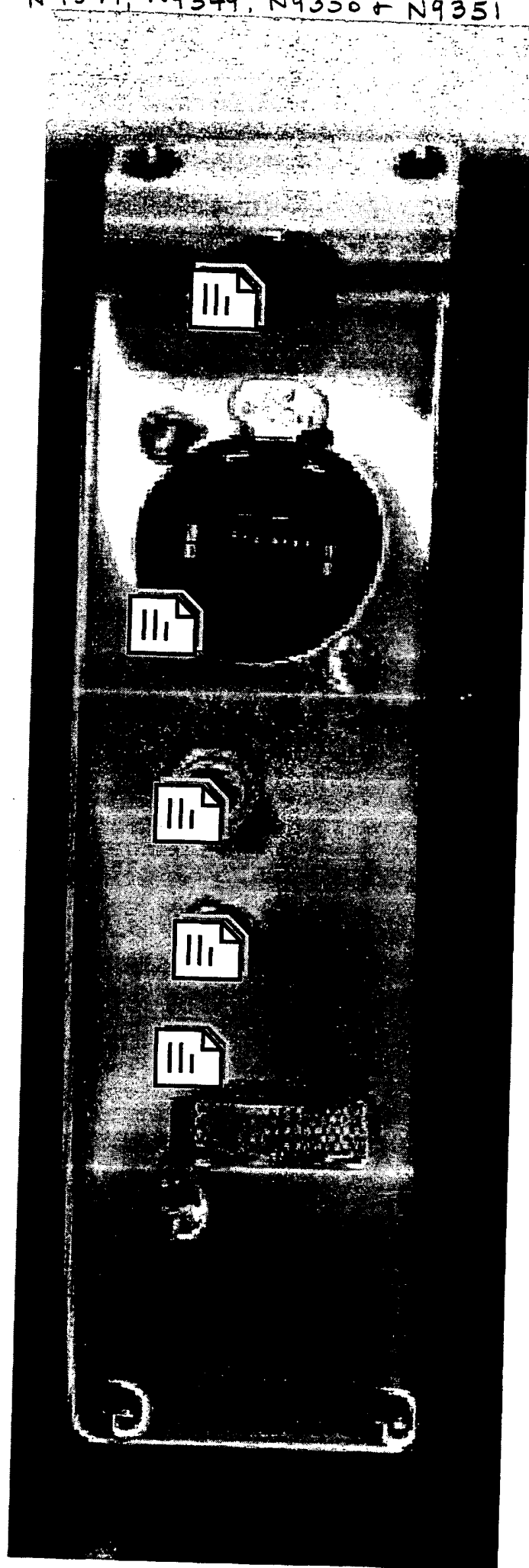


FIG. 21C

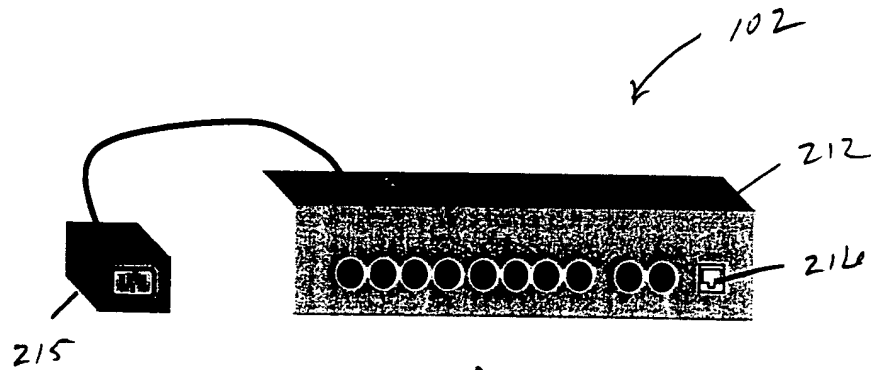
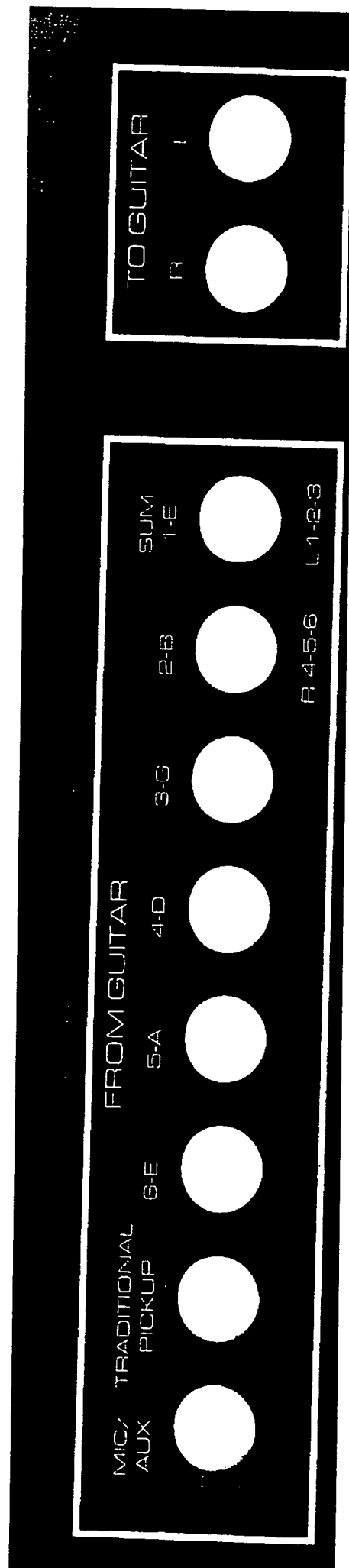
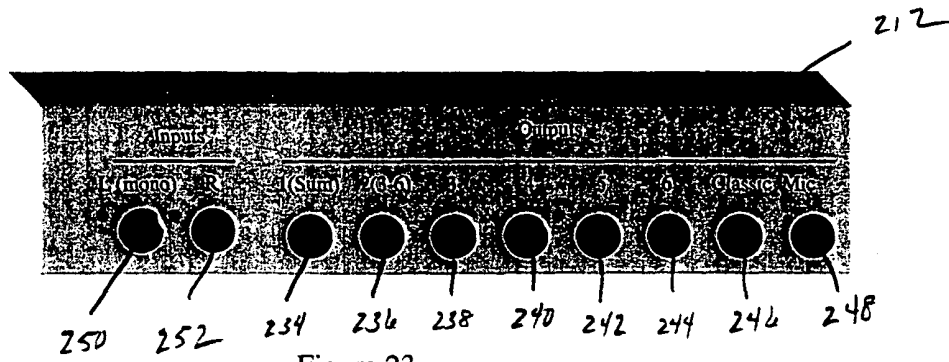


Figure 22 A







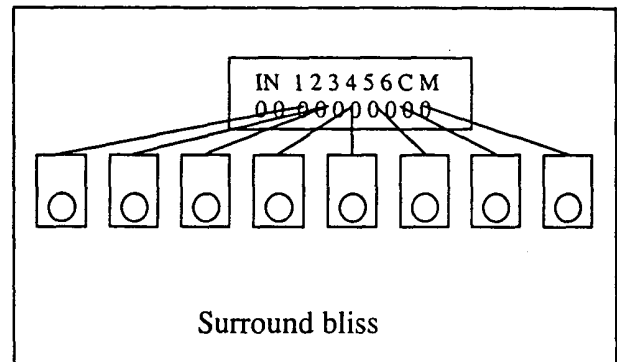
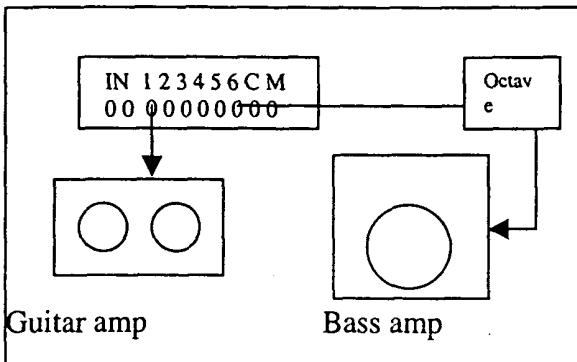
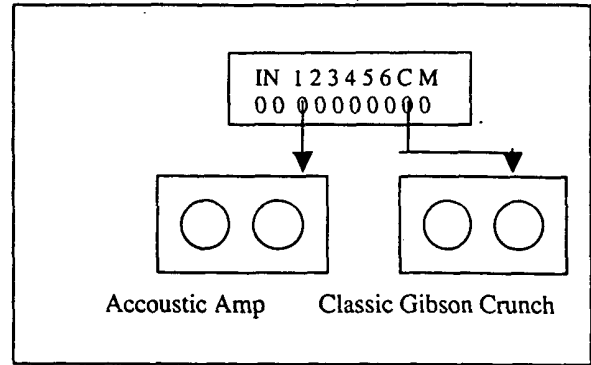
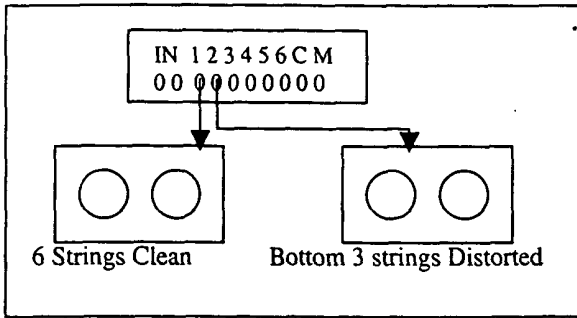


Figure 24

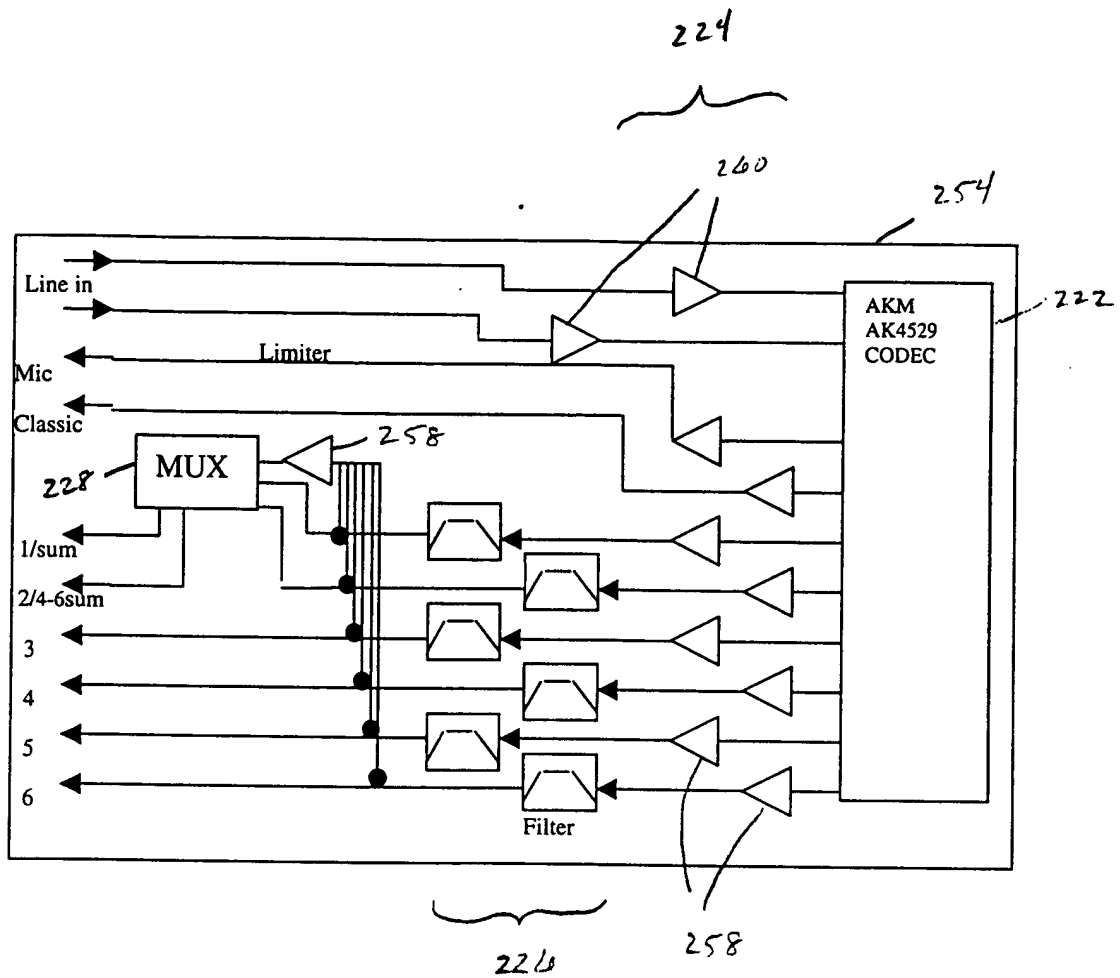


Figure 25

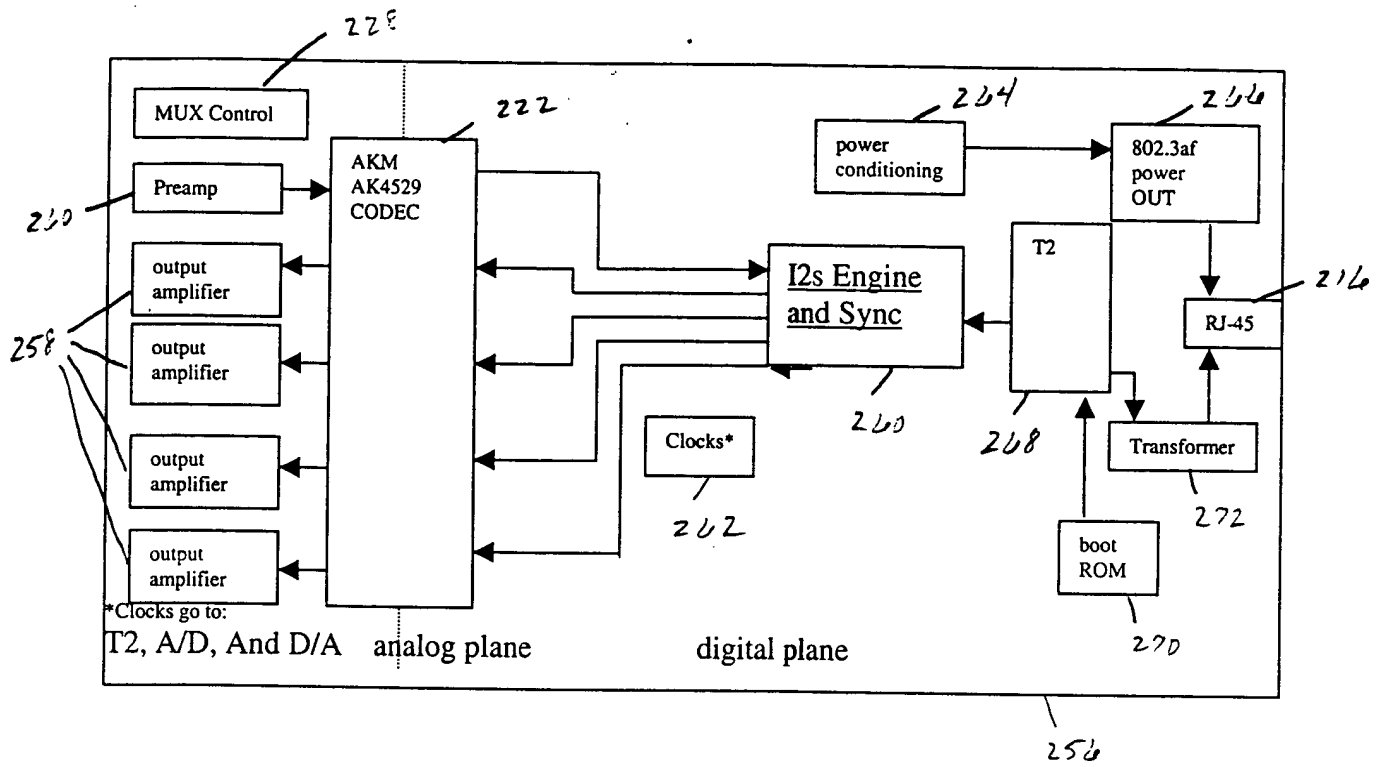


Figure 26

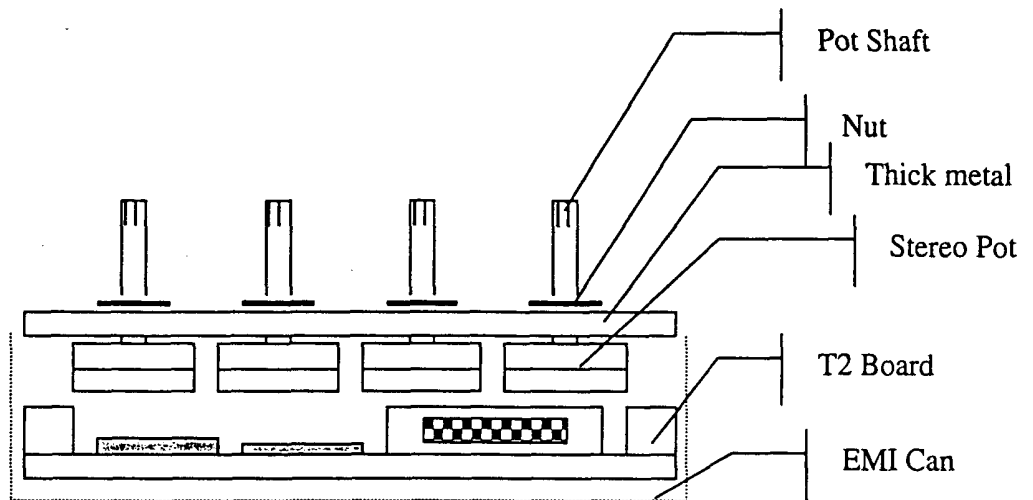


Figure 27

# Passive Legacy system and Control Section

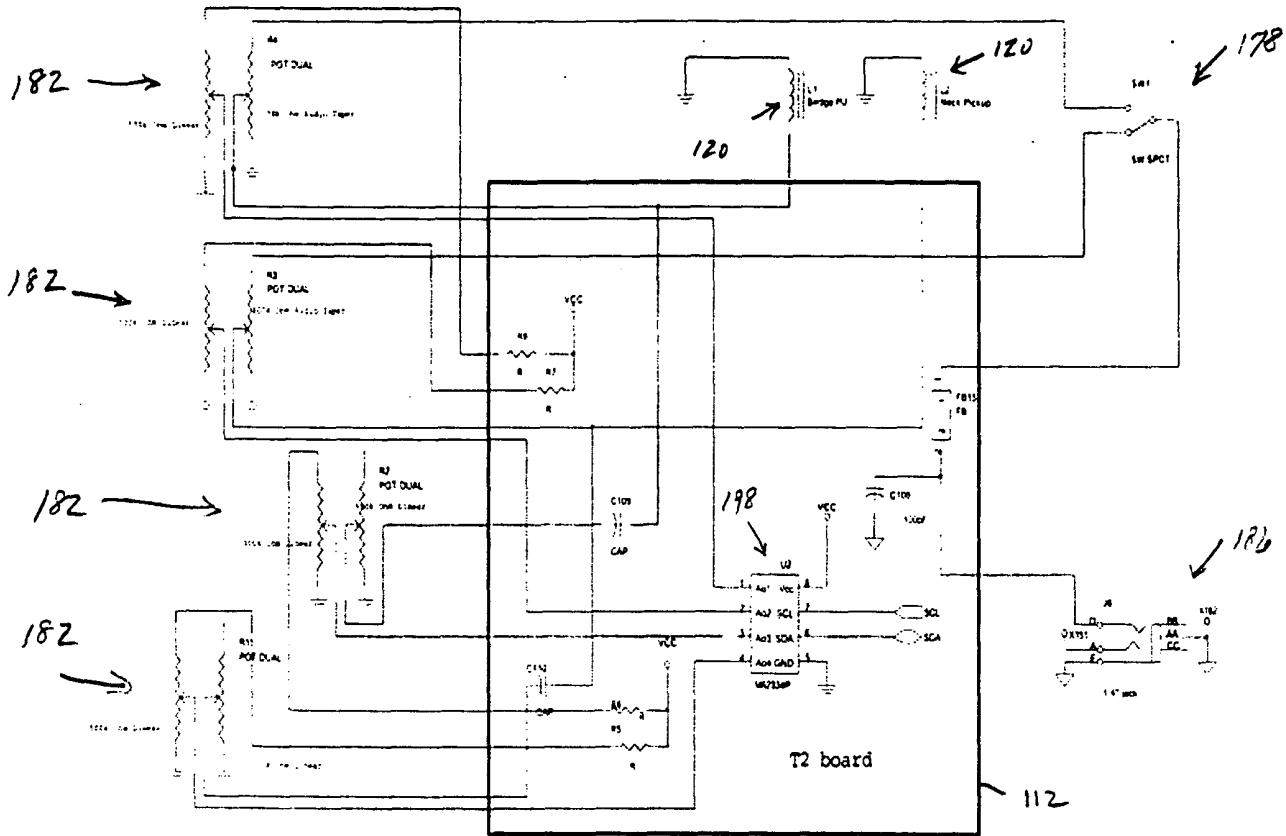
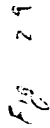


Figure 28



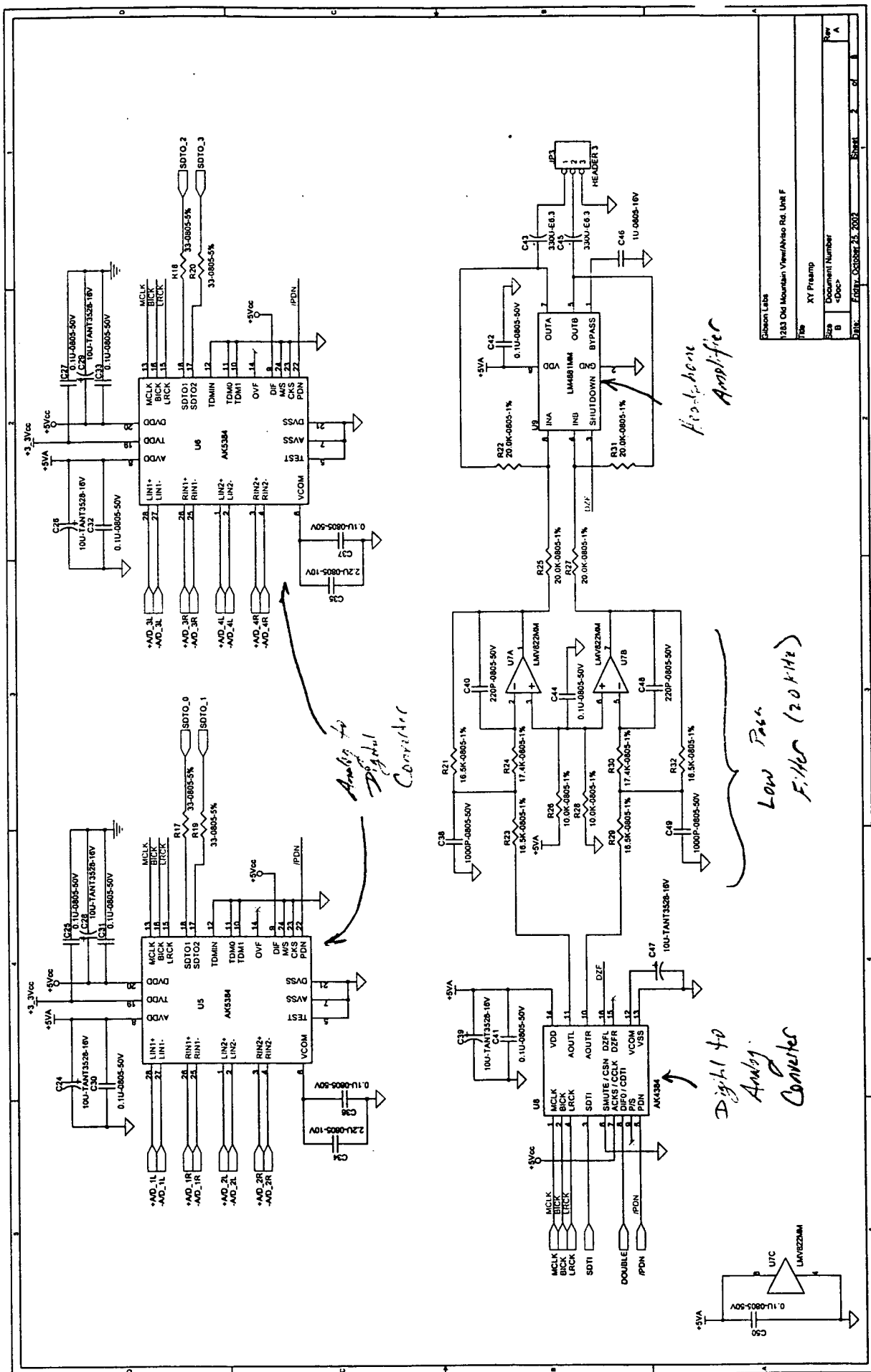
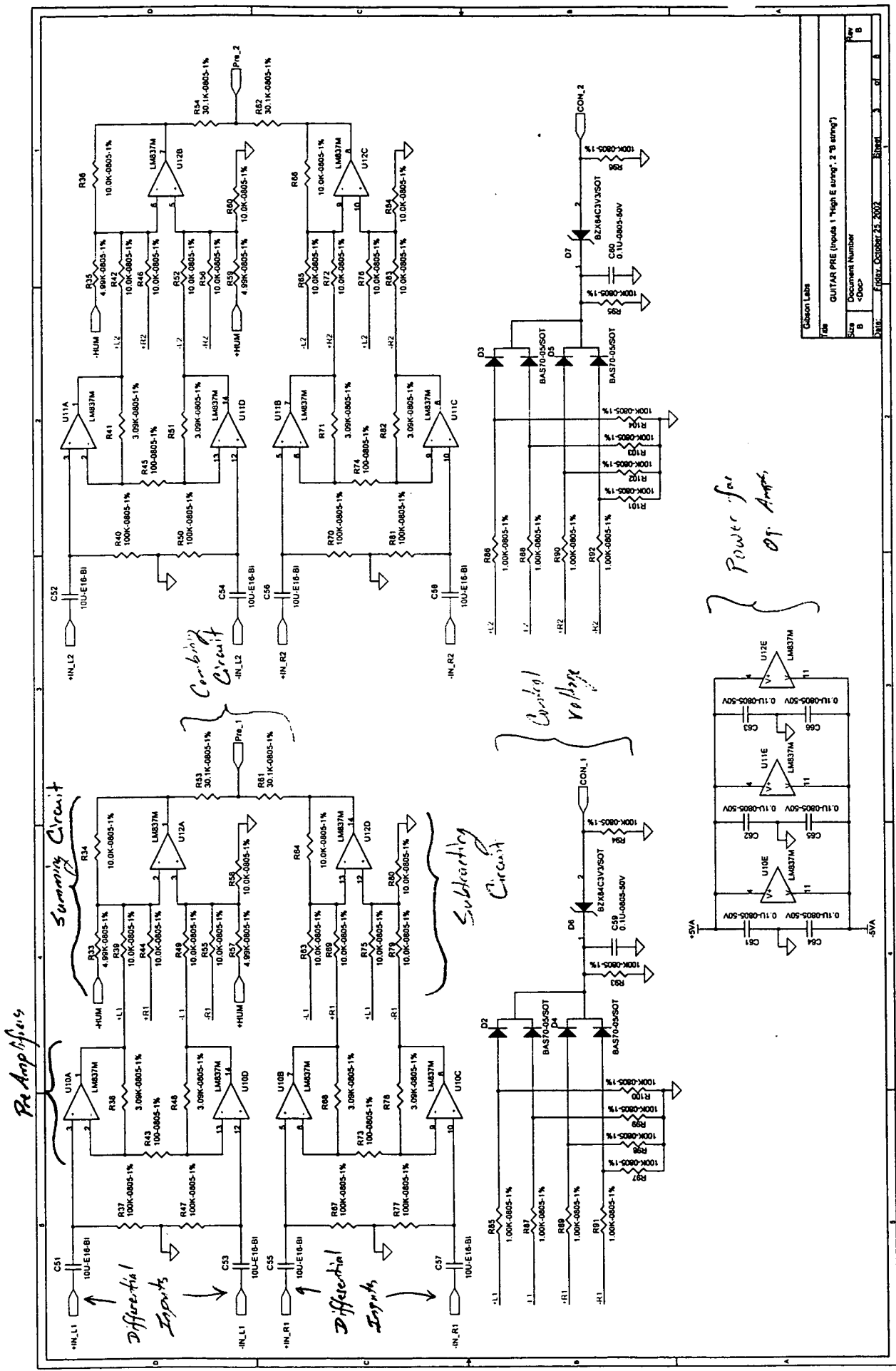


Fig. 30



Power for  
of Amps

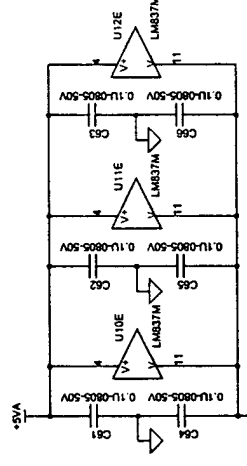
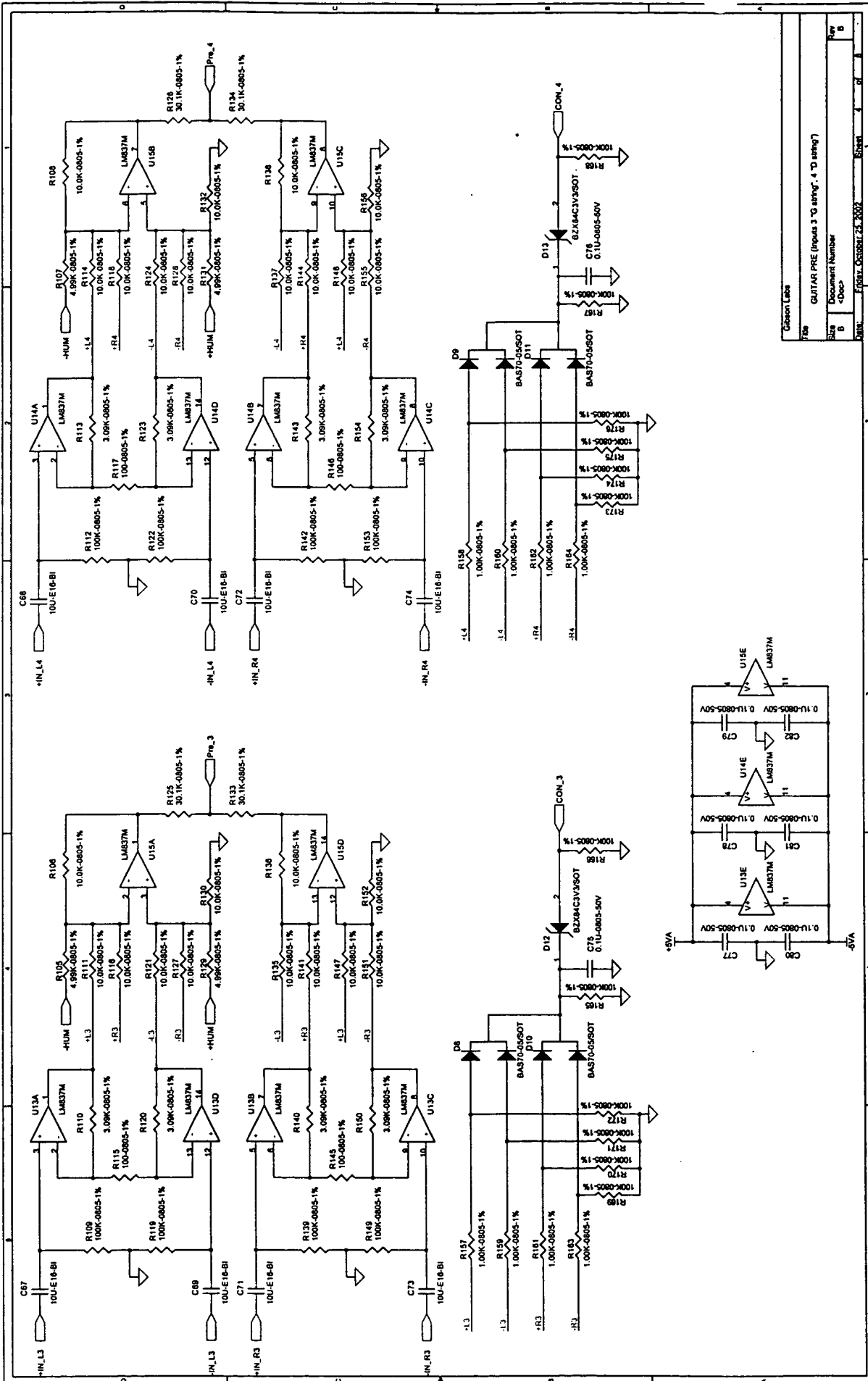
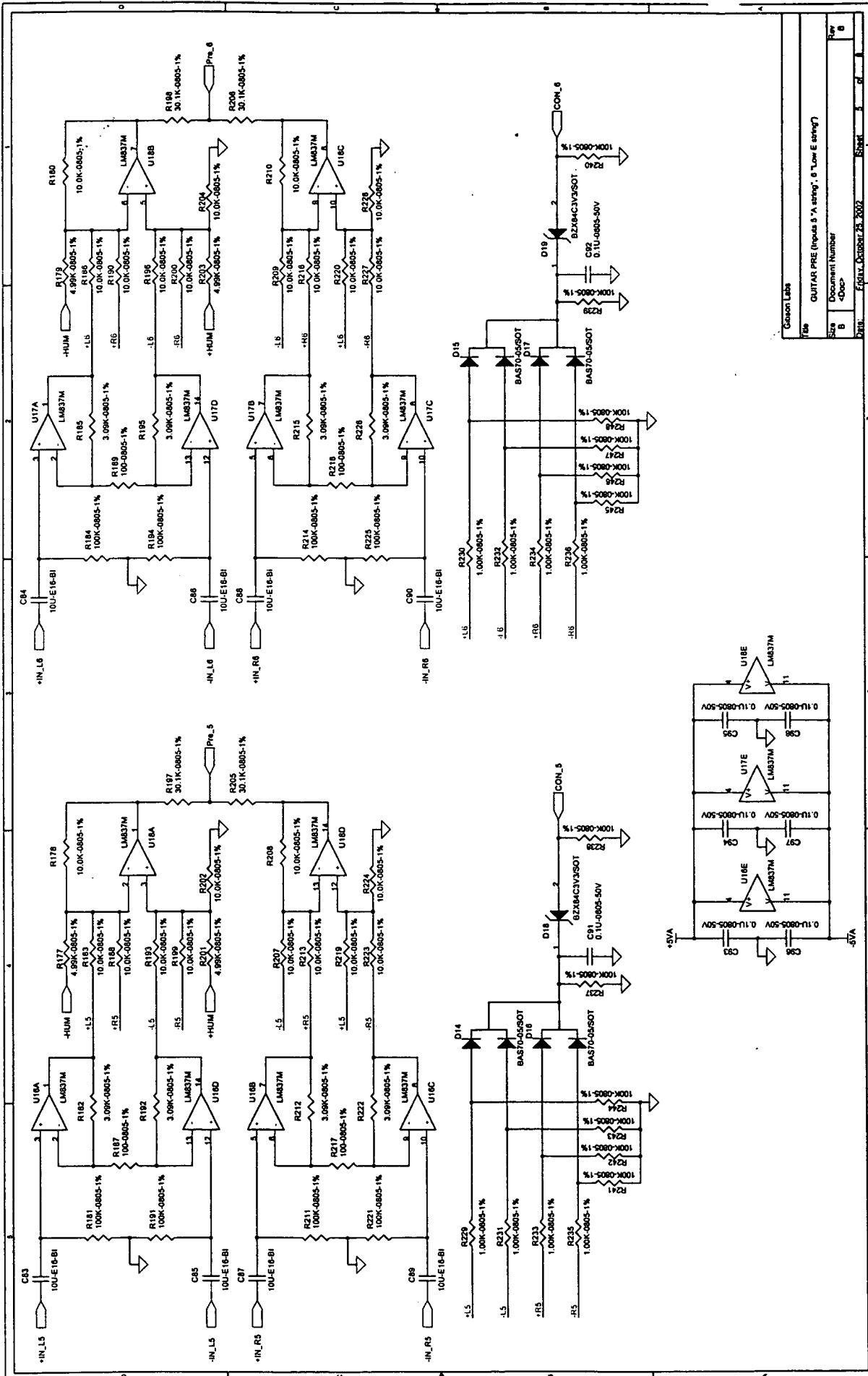


Fig. 31

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Doc	Doc Number	Doc Date	Doc Rev
Doc	Doc Number	Doc Date	Doc Rev
Doc	Doc Number	Doc Date	Doc Rev







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Fig. 33

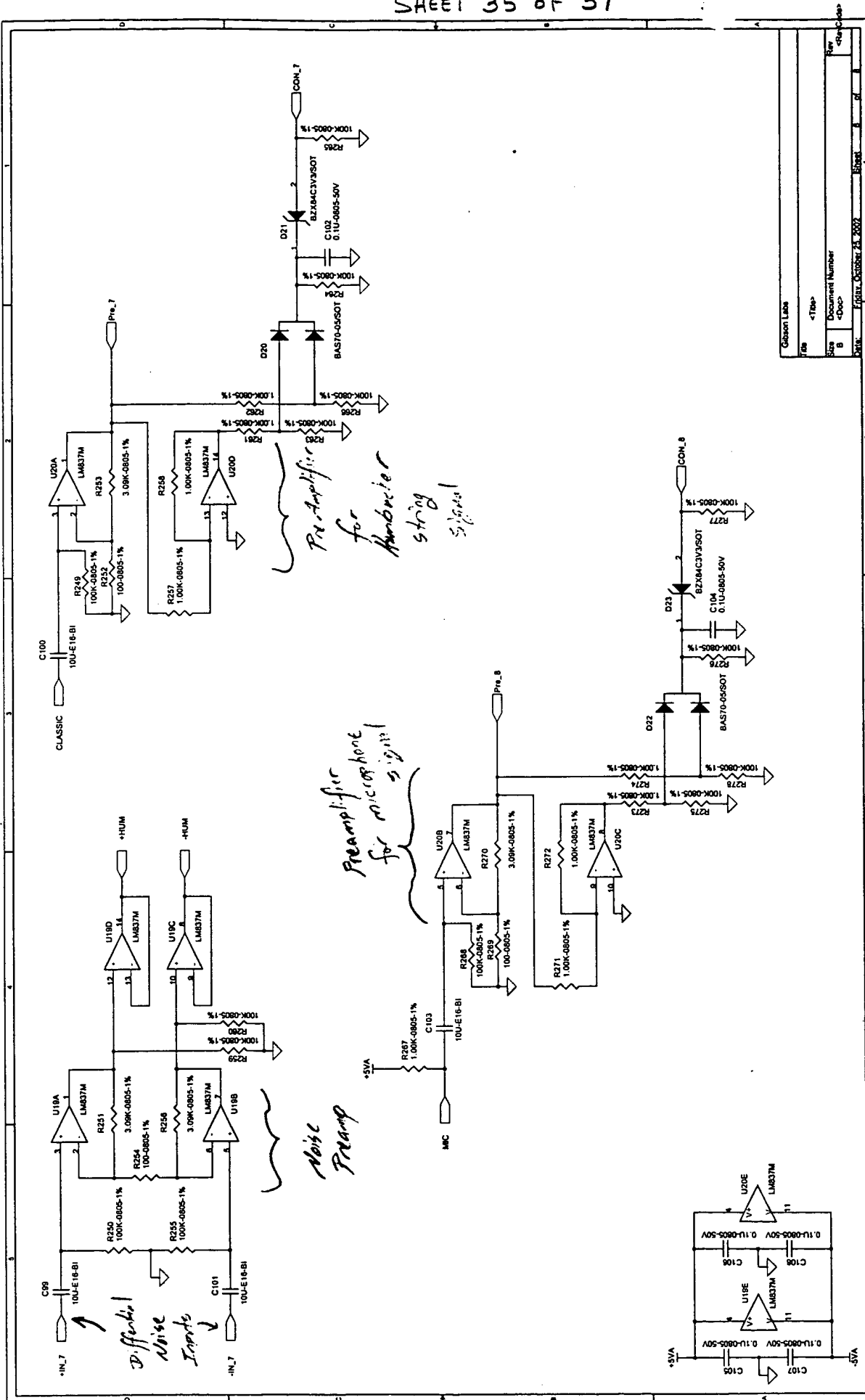


Fig. 34

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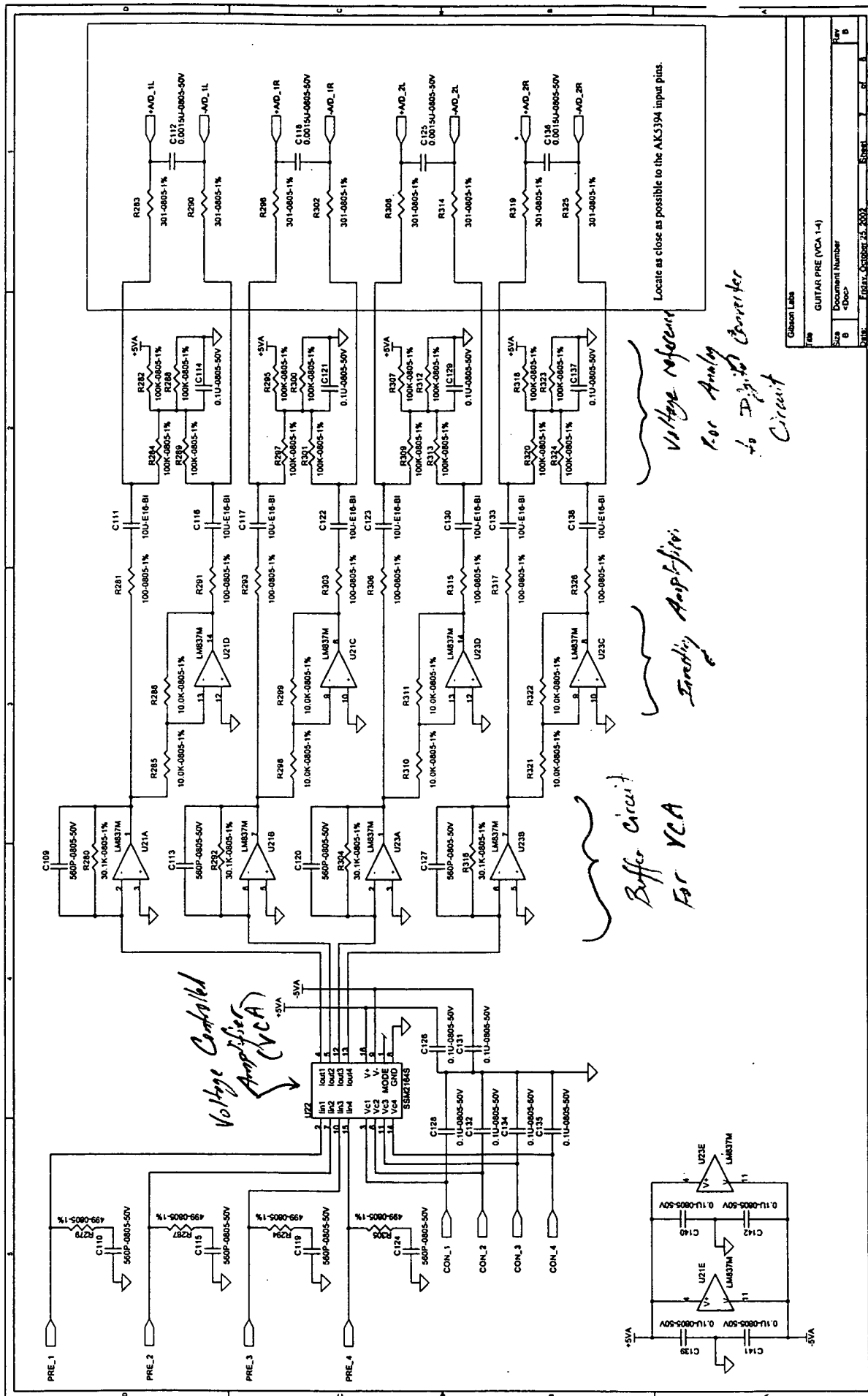


Fig. 35

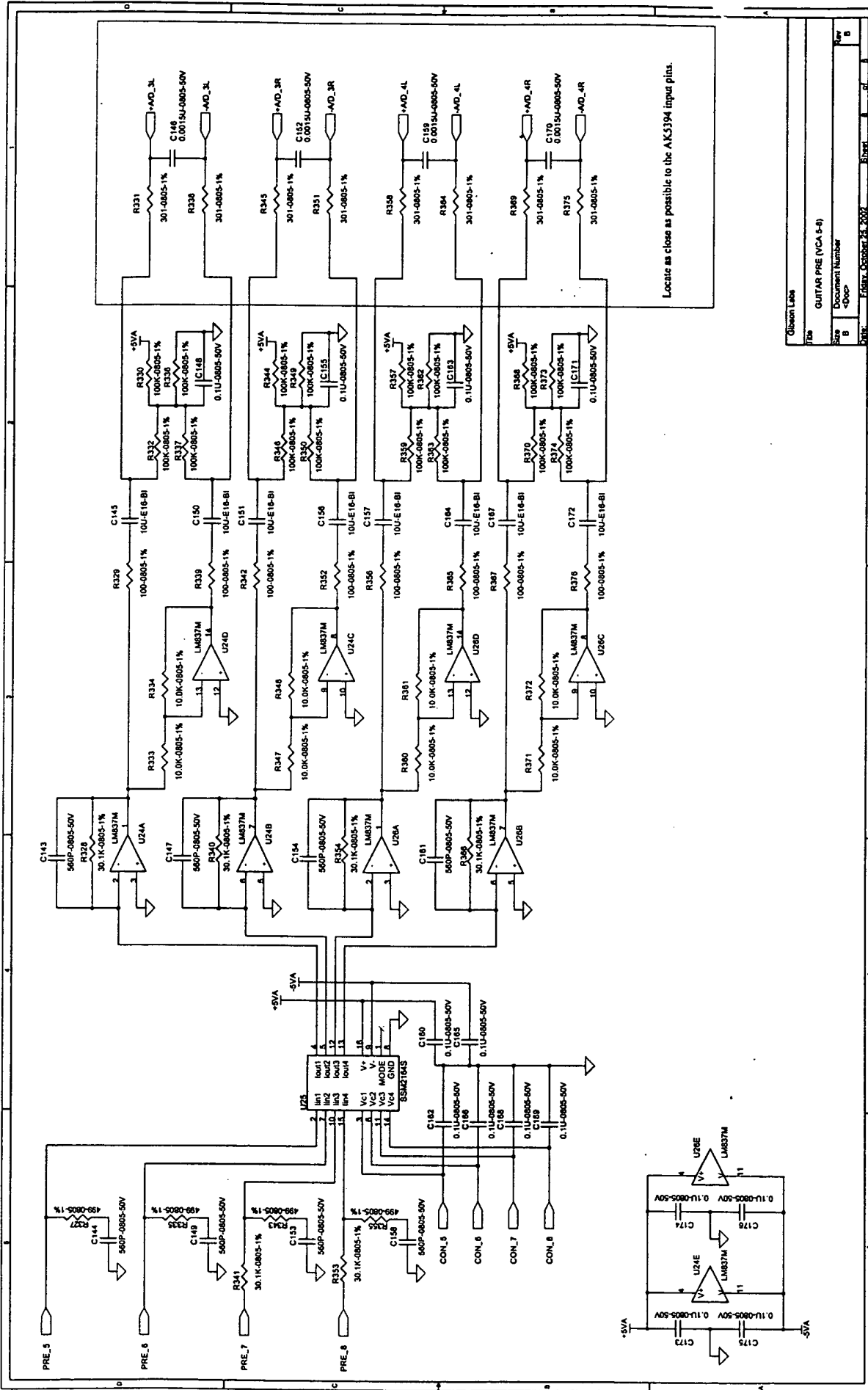


Fig 34

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